

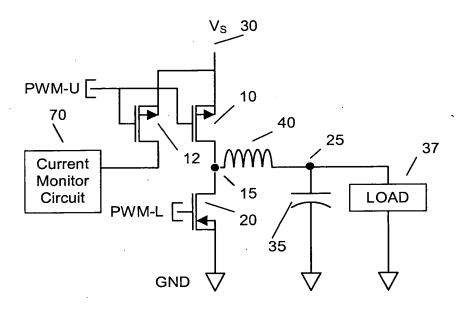
FIG 2 (Prior Art)

Current Monitor Circuit

PWM-L

One of the position of the

FIG 1 (Prior Art)



Condition Responsive Control PWM-U V<sub>s</sub> 30 Circuit 80 TDM TDM 120 10 100 -FIG 4 25 37 Current Monitor 15 Circuit LOAD 20 35 GND

FIG 3 (Prior Art)

R<sub>DSON62</sub>

to Phase 2